

# SILICON POWER TRANSISTOR 2SA1412-Z

# PNP SILICON TRIPLE DIFFUSED TRANSISTOR

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#### **DESCRIPTION**

The 2SA1412-Z is designed for High Voltage Switching, especially in Hybrid Integrated Circuits.

#### **FEATURES**

- High Voltage: VcEo = -400 V
- High Speed:  $t_f \le 0.7 \ \mu s$
- · Complement to 2SC3631-Z

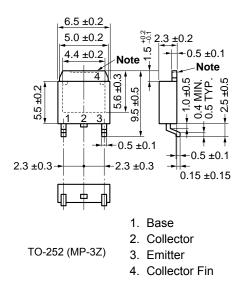
# ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Collector to base voltage	Vcво	-400	V
Collector to emitter voltage	VCEO	-400	V
Base to emitter voltage	$V_{EBO}$	-7	V
Collector current (DC)	Ic(DC)	-2.0	Α
Collector current (pulse) Note 1	Ic(pulse)	-4.0	Α
Total power dissipation (T <sub>A</sub> = 25°C) Note 2	Рт	2.0	W
Junction temperature	$T_{j}$	150	°C
Storage temperature	$T_{stg}$	-55 to +150	°C

#### **Notes 1.** PW $\leq$ 10 ms, Duty Cycle $\leq$ 50%

2. When mounted on ceramic substrate of 7.5 cm $^2 \times 0.7$  mm

# PACKAGE DRAWING (Unit: mm)



**Note** The depth of notch at the top of the fin is from 0 to 0.2 mm.

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# **ELECTRICAL CHARACTERISTICS (Ta = 25 °C)**

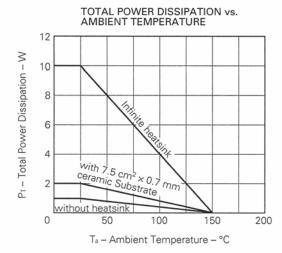
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	Ісво			-10	μΑ	Vcb = -400 V, IE = 0
Emitter Cutoff Current	Ієво			-10	μΑ	VEB = -5.0 V, Ic = 0
DC Current Gain	hFE1*	40	60	120		Vce = -5.0 V, Ic = -0.1 A
DC Current Gain	h <sub>FE2</sub> *	10	. 22			Vce = -5.0 V, Ic = -1.0 A
Collector Saturation Voltage	VcE(sat)*		-0.25	-0.5	V	Ic = -0.5 A, IB = -0.1 A
Base Saturation Voltage	V <sub>BE(sat)</sub> *		-0.85	-1.2	V	Ic = -0.5 A, I <sub>B</sub> = -0.1 A
Gain Bandwidth Product	f⊤		40		MHz	Vce = -10 V, Ie = -100 mA
Output Capacitance	Соь		30		pF	VcB = −10 V, IE = 0, f = 1.0 MHz
Turn-on Time	ton		0.03	0.5	μs	Ic = -1.0 A, R <sub>L</sub> = 150 Ω
Storage Time	tstg		1.4	2.0	μs	$I_{B1} = -1_{B2} = -0.2 \text{ A},$
Fall time	tf		0.1	0.7	μs	Vcc = −150 V

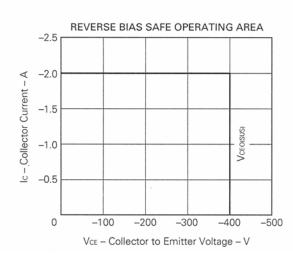
<sup>\*</sup> Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2 %

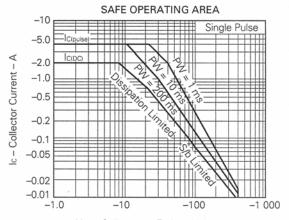
#### hre Classification

MARKING	L	K		
hFE1	40 to 80	60 to 120		

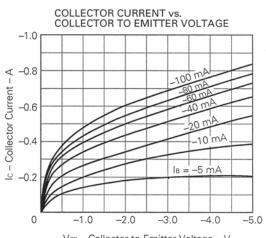
# TYPICAL CHARACTERISTICS (Ta = 25 °C)



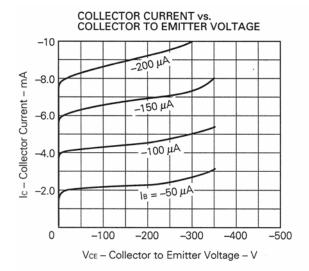




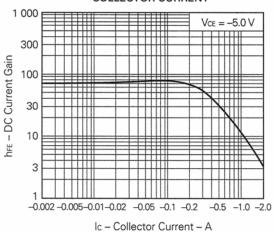
 $V_{\text{CE}} - Collector \ to \ Emitter \ Voltage - V$ 



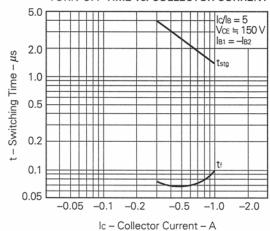
VcE - Collector to Emitter Voltage - V



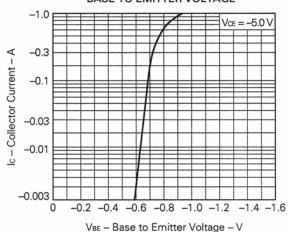




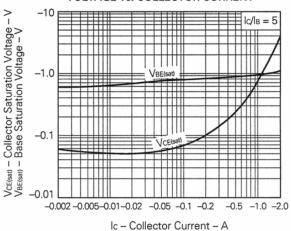
### TURN-OFF TIME vs. COLLECTOR CURRENT



#### COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



# COLLECTOR AND BASE SATURATION VOLTAGE vs. COLLECTOR CURRENT



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